In re Patent Application of:
APPLETON ET AL
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IN THE SPECIFICATION:

Please replace paragraph [00012] bridging pages 7 and 8, with the following rewritten paragraph:

[00012] More particularly, the motherboard 10 is provided with a standard daughtercard receptacle 16 that is configured to receive and be engaged by a corresponding card insertion element or terminal edge connector 26 mounted on a daughtercard 20. As pointed out briefly above, the daughtercard contains its own application flash memory 23, which contains operational firmware associated with on-board hardware 24, such as a prescribed communication transceiver interface (e.g. a DS3 communication interface, Ethernet interface, and the like). The application firmware contained in the flash memory 23 on the daughtercard is based upon and is effective to control operation of the daughtercard's on-board transceiver (hardware) 24, as well as the overall operation of the motherboard in executing the functionality of the daughtercard's firmware. Namely, the daughtercard-resident firmware is intended to be executed by the motherboard processor 12 'in place of' the base system application firmware contained the default flash memory 14 of the motherboard. (As described above, ~~This~~this is in contrast to a conventional system which, continues to perform all of its previous functions when a daughtercard has been added.)

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